

**Amendments to the Drawings**

Fig. 1 has been amended to include reference number 10.

Fig. 5B has been amended to replace reference number 181 with reference number 184.

Fig. 5C has been amended to replace reference number 181 with reference number 184, and to replace reference number 208 with reference number 205.

Attachment: Replacement Sheet  
Annotated Marked-Up Drawings

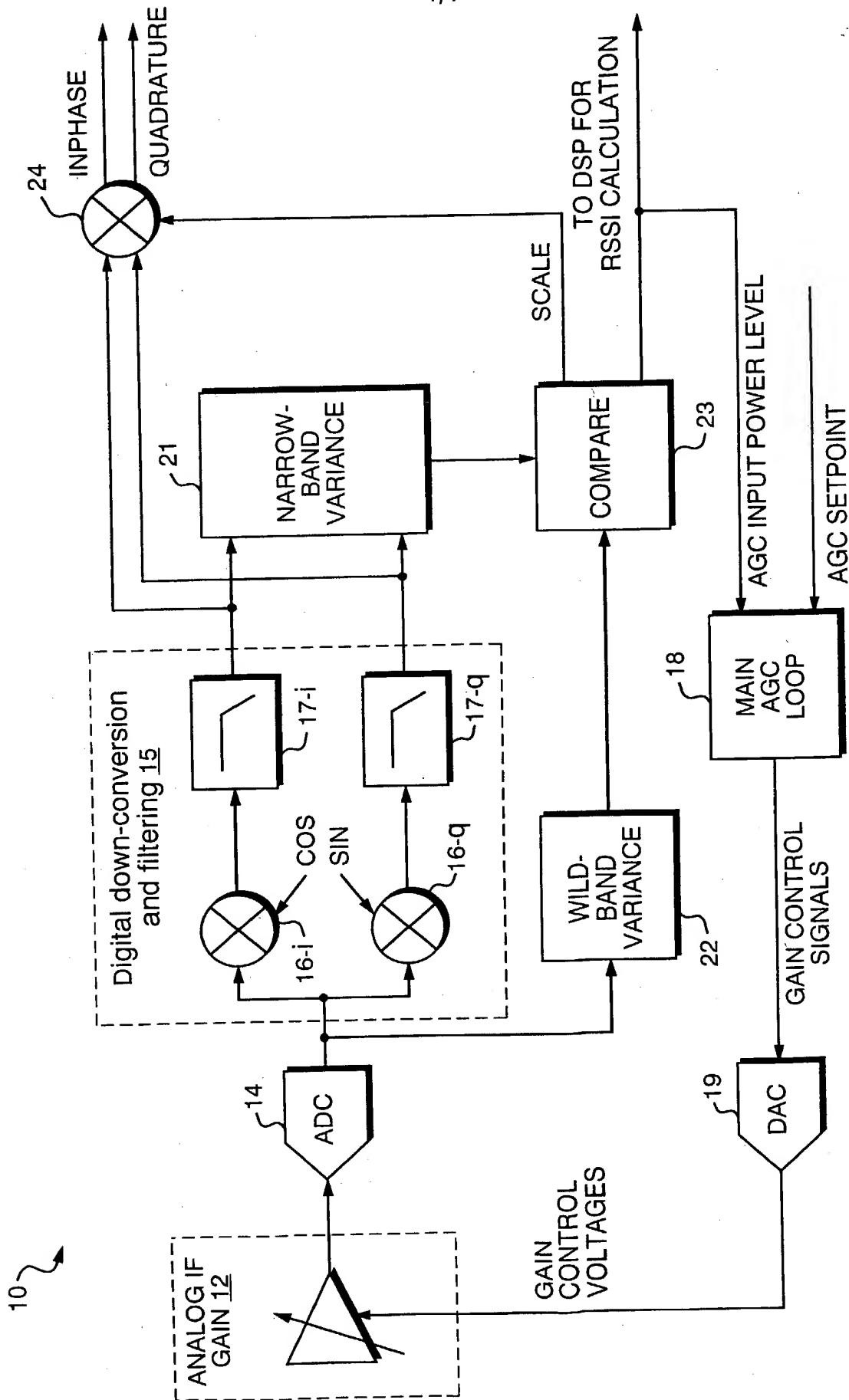
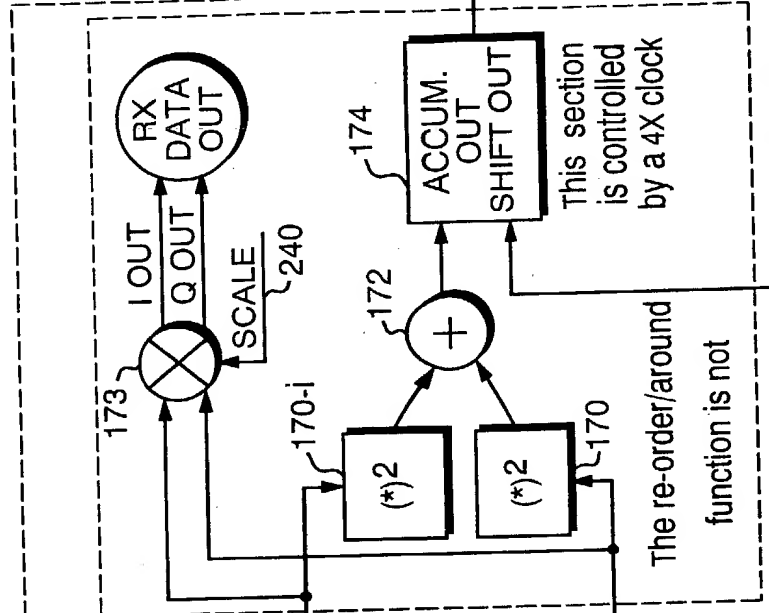
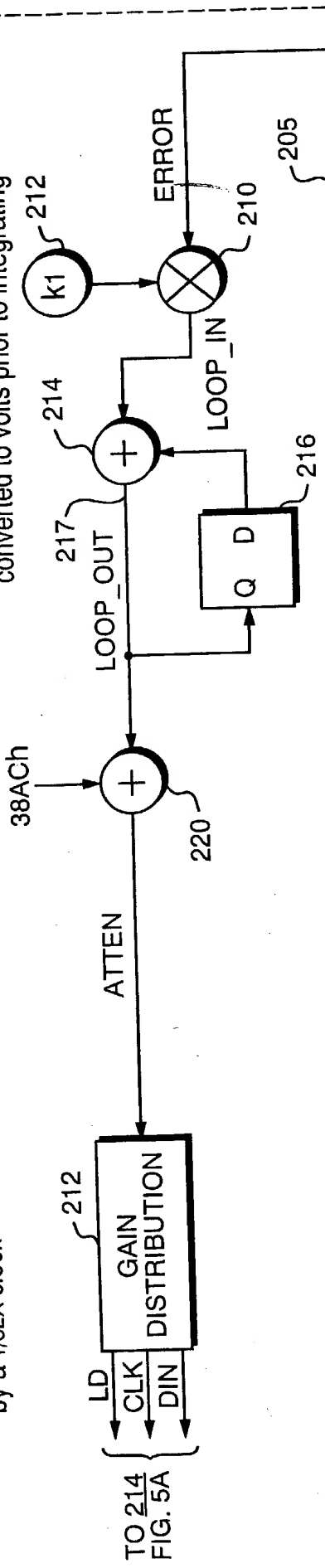


FIG. 1

5/7

Since the gain control voltage is linear-linear, filtering the RMS power in dB is possible. Otherwise, the data would need to be converted to volts prior to integrating

This section is controlled by a 1/32X clock



CIC gain values (based on decimation and expected input levels). See AD6620 data sheet for details

FROM 14 FIG. 5A

FIG. 5B

FROM 184 FIG. 5C

FROM 1/32X Clock FIG. 5C

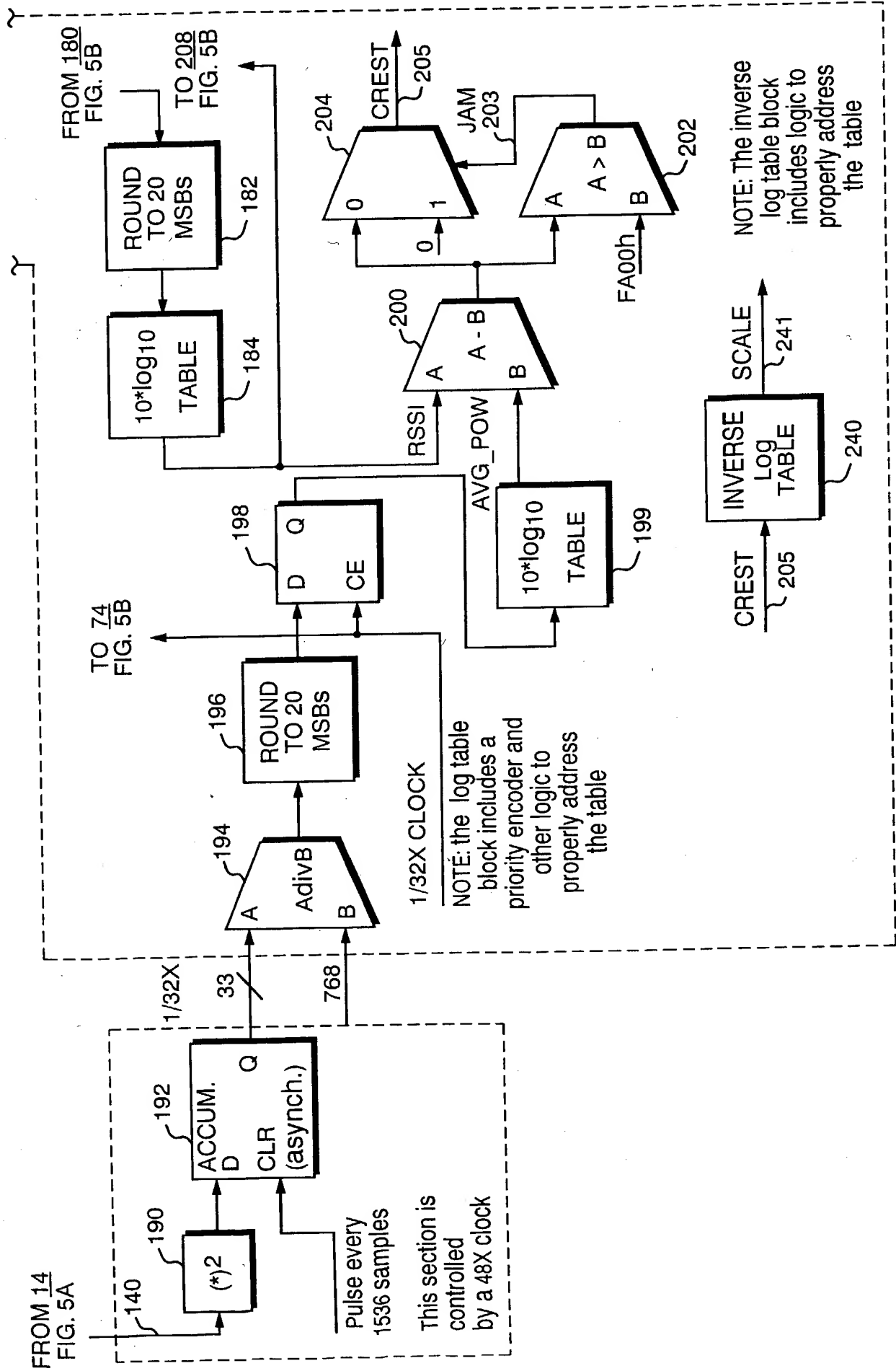


FIG. 5C